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**FACSIMILE COVER SHEET**

To Commissioner for Patents	Total Pages Sent:	3
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Facsimile Number: (703) 746-4000	Transmission Date:	March 30, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kowalski, *et al.* Docket No.: INF-125  
Serial No: 10/748,332 Art Unit: 2815  
Date Filed: December 31, 2003  
Title: Device Architecture and Process for Improved Vertical Memory Arrays

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

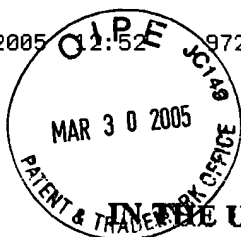
- Certification of Facsimile Transmission (1 page)
- Submission of Paper Incorporated by Reference (1 page)
- Cover Page for U.S. Patent No. 5,519,236

Respectfully submitted,

Kristin R. Hayes  
Legal Assistant

**Confirmation Respectfully Requested**

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Applicant: Kowalski, *et al.* Docket No.: INF-125  
Filed: December 31, 2003 Examiner: Warren, Matthew E.  
Serial No.: 10/748,332 Art Unit: 2815  
For: Device Architecture and Process for Improved Vertical Memory Arrays

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUBMISSION OF PAPER THAT IS INCORPORATED BY REFERENCE**

Dear Sir:

Applicant provides herewith a copy of the cover page for U.S. Patent No. 5,519,236 that is cited in paragraphs 0002 and 0015 of the application. Since this patent is not material to the patentability of any of the claims, Applicant believes that examination of this reference is not necessary. However, since the patent has been incorporated by reference, and therefore makes up part of the disclosure, Applicant requests that a copy of this reference be placed in the file.

March 30, 2005

Date

Respectfully submitted,

Ira S. Matsil  
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